V10DM60C

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Vishay General Semiconductor

Dual Low-Voltage Trench MOS Barrier Schottky Rectifier

Ultra Low $V_F = 0.42$ V at $I_F = 2.5$ A



V10DM60C

PRIMARY CHARACTERISTICS				
I _{F(AV)}	2 x 5 A			
V _{RRM}	60 V			
I _{FSM}	80 A			
V_F at I_F = 5 A (T_A = 125 °C)	0.52 V			
T _J max.	175 °C			
Package	SMPD (TO-263AC)			
Diode variations	Common cathode			

FEATURES

- Trench MOS Schottky technology
- Very low profile typical height of 1.7 mm
- Ideal for automated placement
- Low forward voltage drop, low power losses
- · High efficiency operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified available:
 Automotive ordering code: base P/NHM3
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

TYPICAL APPLICATIONS

For use in high frequency DC/DC converters, switching power supplies, freewheeling diodes, OR-ing diode, and reverse battery protection in commercial, industrial, and automotive application.

MECHANICAL DATA

Case: SMPD (TO-263AC)

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant Base P/NHM3 - halogen-free, RoHS-compliant, and AEC-Q101 qualified

Terminals: matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 and HM3 suffix meet JESD 201 class 2 whisker test **Polarity:** as marked

MAXIMUM RATINGS (T _A = 25 °C unless otherwise noted)					
PARAMETER		SYMBOL	V10DM60C	UNIT	
Device marking code			V10DM60C		
Maximum repetitive peak reverse voltage		V _{RRM}	60	V	
Maximum average forward rectified current (fig. 1)	per device	I _{F(AV)} ⁽¹⁾	10	Δ	
	per diode		5	A	
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load		I _{FSM}	80	А	
Operating junction temperature range		T _J ⁽²⁾	-40 to +175	℃	
Storage temperature range		T _{STG}	-55 to +175		

Notes

⁽¹⁾ Mounted on infinite heatsink

 $^{(2)}$ The heat generated must be less than the thermal conductivity from junction-to-ambient: dP_D/dT_J < 1/R_{0JA}

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RoHS

COMPLIANT



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ELECTRICAL CHARACTERISTICS ($T_A = 25$ °C unless otherwise noted)						
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT
Instantaneous forward voltage per diode	I _F = 2.5 A	T_ = 25 °C	V _F ⁽¹⁾	0.52	-	V
	I _F = 5 A			0.58	0.66	
	I _F = 2.5 A	– T _A = 125 °C		0.42	-	
	I _F = 5 A			0.52	0.60	
Reverse current at rated V_R per diode	V _R = 60 V	T _A = 25 °C	I _R ⁽²⁾	-	0.25	- mA
	v _R = 00 v	T _A = 125 °C		1.2	7	
Typical junction capacitance	4.0 V, 1 MHz		CJ	570	-	pF

Notes

 $^{(1)}\,$ Pulse test: 300 μs pulse width, 1 % duty cycle

 $^{(2)}~$ Pulse test: Pulse width $\leq 5~ms$

THERMAL CHARACTERISTICS ($T_A = 25 \text{ °C}$ unless otherwise noted)				
PARAMETER	SYMBOL	V10DM60C	UNIT	
Typical thermal resistance per device	R _{0JC} ⁽¹⁾	2.5	°C/W	
	R _{0JA} (2)(3)	58	0/11	

Notes

⁽¹⁾ Mounted on infinite heatsink

 $\label{eq:linear} ^{(2)} \mbox{ The heat generated must be less than the thermal conductivity from junction-to-ambient: } dP_D/dT_J < 1/R_{\theta JA} \mbox{ - junction-to-ambient: } dP_D/dT_J \mbox{ - junction-to-$

⁽³⁾ Free air, without heatsink

ORDERING INFORMATION (Example)						
PREFERRED P/N	PREFERRED P/N UNIT WEIGHT (g) PACKAGE CODE BASE QU		BASE QUANTITY	DELIVERY MODE		
V10DM60C-M3/I	0.55	I	2000/reel	13" diameter plastic tape and reel		
V10DM60CHM3/I (1)	0.55	I	2000/reel	13" diameter plastic tape and reel		

Note

⁽¹⁾ AEC-Q101 qualified

V10DM60C



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RATINGS AND CHARACTERISTICS CURVES ($T_A = 25$ °C unless otherwise noted)

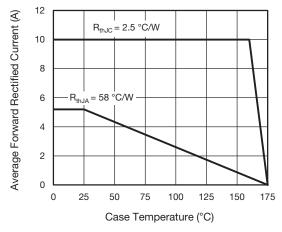


Fig. 1 - Maximum Forward Current Derating Curve

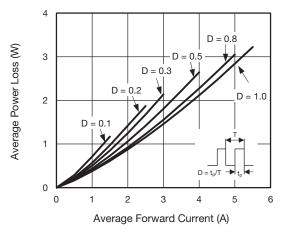


Fig. 2 - Average Power Loss Characteristics

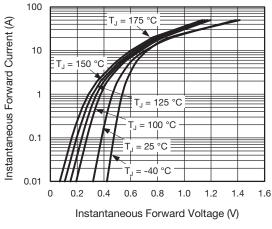


Fig. 3 - Typical Instantaneous Forward Characteristics

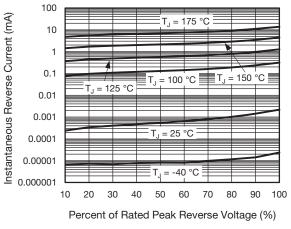


Fig. 4 - Typical Reverse Leakage Characteristics

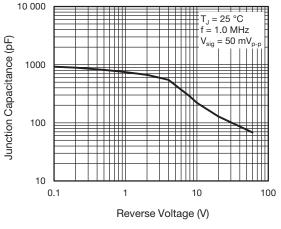


Fig. 5 - Typical Junction Capacitance

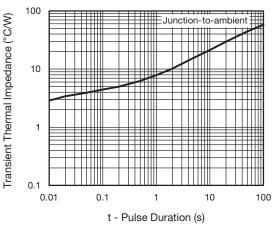


Fig. 6 - Typical Transient Thermal Impedance

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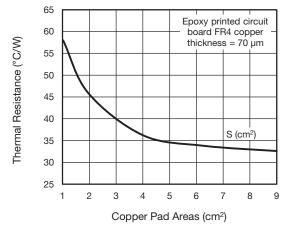
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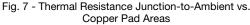
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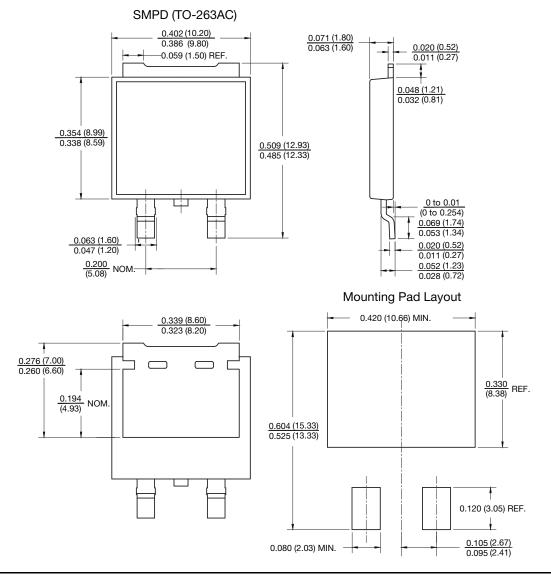


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PACKAGE OUTLINE DIMENSIONS in inches (millimeters)



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