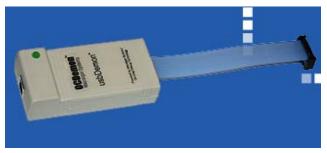
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The usbWiggler™ is a mid-cost interface used in the design, debug, and programming of microprocessor based embedded systems. One side of the usbWiggler interfaces to the USB port of a host IBM compatible PC and the other side connects to an OCD (On-Chip Debug) port of the target system. This port may be JTAG, E-JTAG, OnCE, COP, BDM or any of several other types of connections. When connected to a USB 2.0 port on your PC, the usbWiggler will operate up to Hi-Speed USB rates (480Mb/s). 32-bit Windows platforms only at this time.

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